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Synthesizing genetic sequential logic circuit with clock pulse generator

Chia-Hua Chuang and Chun-Liang Lin*

Abstract

Background: Rhythmic clock widely occurs in biological systems which controls several aspects of cell physiology. For the different cell types, it is supplied with various rhythmic frequencies. How to synthesize a specific clock signal is a preliminary but a necessary step to further development of a biological computer in the future.

Results: This paper presents a genetic sequential logic circuit with a clock pulse generator based on a synthesized genetic oscillator, which generates a consecutive clock signal whose frequency is an inverse integer multiple to that of the genetic oscillator. An analogous electronic waveform-shaping circuit is constructed by a series of genetic buffers to shape logic high/low levels of an oscillation input in a basic sinusoidal cycle and generate a pulse-width-modulated (PWM) output with various duty cycles. By controlling the threshold level of the genetic buffer, a genetic clock pulse signal with its frequency consistent to the genetic oscillator is synthesized. A synchronous genetic counter circuit based on the topology of the digital sequential logic circuit is triggered by the clock pulse to synthesize the clock signal with an inverse multiple frequency to the genetic oscillator. The function acts like a frequency divider in electronic circuits which plays a key role in the sequential logic circuit with specific operational frequency.

Conclusions: A cascaded genetic logic circuit generating clock pulse signals is proposed. Based on analogous implement of digital sequential logic circuits, genetic sequential logic circuits can be constructed by the proposed approach to generate various clock signals from an oscillation signal.

Keywords: Genetic oscillator, Clock signal, Logic circuit, Optimization

Background

Synthetic biology is an emerging interdisciplinary research field, which concentrates on understanding the behaviors of biological system from system-level as well as creating an artificial genetic circuit based on the principles of systems biology, mathematics and engineering [1-4]. Analogous to an electronic circuit, the synthetic genetic circuit also includes some standard biological components to assemble the biochemical process of living organisms and achieve specific functionality. Based on a bottom-up approach, more complicated bio-computing modules can be expected to perform more complex functions via integrating a variety of biological devices, like very-large-scale integration circuits in electronics. By using mathematical models to capture the quantitative and qualitative characteristics of biological systems, the customized genetic circuits with specific functions can be designed

from the system perspective [5-8]. For drug development and disease treatment, synthetic biology brings a useful and rapid direction through inserting the designed genetic circuits into the host cells to improve or modify the disease state of organisms. In addition, there are still potential applications in biofuels, biotechnology, bioremediation, and bioenergy remained to be developed.

Inspired by electronic circuits, several synthetic genetic circuits have recently been created, such as toggle switch, genetic oscillator, pulse generator, genetic counter, logic evaluator, sensor, filter, and cell-cell communicator. The former two are based on protein-protein interaction without any external input to control their behaviors. Toggle switch applies two repressor genes repressing each other to cause bi-stable phenomenon, like as a memory device [9]. By cascading odd number of repressor genes in the cycle chain, a genetic oscillator can be synthesized to generate a stable oscillation signal in the protein response and applied in the control of dosage of drugs, or regarded as a synchronous mechanism for cell-cell communication [10-13].

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A pulse generator generates an instantaneous stimulating signal and then resets to the original state by using time difference between the input and the corresponding delayed signals [14,15]. If the input signal is a periodic clock signal, then a clock pulse signal can be synthesized. Biosensor and filter are designed to detect the concentrations of specific molecular signal and range [8].

Boolean logic gate is an essential unit of a computer in digital logic circuits. To bring the insight of digital logic circuit design in electronic systems into biological systems, the more complicated bio-computing processes can be easily constructed by combining a variety of genetic logic gates. The genetic logic gates constructed are based on different genetic transcriptional reactions to express various logic behaviors [16-22]. To use genetic components such as promoter, ribosomal binding site (RBS), repressor/activator genes and reporter gene, genetic logic gates with different logical operations have been assembled, such as NOT, Buffer, AND, OR, XOR. Through synchronous cascades of these genetic logic gates based on the topology of digital logic circuits, more complicated genetic logic circuits can be synthesized, such as multiplexer, half adder, combinational logic circuits, memory, and sequential logic circuits [23-30]. A genetic sequential logic circuit works with a counters, which is composed of some basic devices such as SR latch and flip-flop, has been developed in [25,29].

In biological systems, there are different rhythm frequencies depending on cell types. A 12-hour rhythm has been recently found in the mouse liver. For this reason, there are many engineered approaches proposed to synthesize the specific oscillation signals. In [31], the frequency-doubling oscillation can be constructed by using Fourier theory. A genetic circuit with multiple functions is designed to synthesize the oscillation signal with half original frequency [32]. Another aspect is to use regulated protein to control the transcription and degradation rates of target gene in an existing network structure [33-35]. A robust synthetic genetic circuit is designed based on H_∞ control theory by regulating degradation rates of mRNAs and proteins in stochastic perturbational environments [33,34]. For cell-cell communication, synchronized genetic circuit designs are proposed to synchronize a population of oscillation signals [36,37]. To construct a promoter-RBS library from microarray data and find suitable promoter-RBS components, a robust genetic circuit has been theoretically realized in the genetic systems by a systematic approach [38,39].

This paper proposes an artificial genetic sequential logic circuit with a function of frequency divider based on the periodic oscillation signal from a repressilator and analogous topology of the digital logic circuits in electronics. The proposed genetic sequential logic circuit is triggered by a clock pulse signal to generate a clock signal whose frequency is an inverse integer multiple to

the genetic oscillator. Similar to an electronic waveform-shaping circuit, a genetic waveform-shaping circuit constructed by several genetic Buffers in series is designed, which regulates time duration of logic high/low levels of an oscillation signal in the basic sinusoidal cycle and reshapes the oscillation signal into a pulse-width-modulated (PWM) signal with different duty cycles by regulating the different threshold levels of the Buffer. The PWM signal can be regarded as a pulse signal with the frequency is coherent to that of the genetic oscillator. The clock pulse signal is served as the rising or falling triggered edges of a clock signal with base frequency. In the digital logic theory, Karnaugh map is applied to determine the input signals of the rising or falling edge-triggered genetic JK flip-flops in each level [40]. A synchronous genetic counter circuit is triggered by the clock pulse signal to realize the genetic clock with its frequency is an inverse integer multiple to the genetic oscillator.

For our proposed genetic pulse generator design, the periodic property of genetic oscillator is considered and the clock pulse signal is generated by utilizing the existing synthetic genetic oscillator constructed by three repressor genes which repress each other in the closed loop. Different from the genetic counter circuit design [25,29], we introduce a generalized form based on the topology of digital logic circuits for synthesizing a clock signal with an inverse multiple of clock frequency to the genetic oscillator. The major advantage of the proposed approach is that it is easy to construct complex genetic sequential logic circuits via bottom-up approach with less computational time. Simulation results *in silico* show performance of the synthesizing clock pulse signal, and the clock signal with double, triple, quadruple basal periods while operating at the same genetic oscillator.

Methods

Dynamic model of synthetic genetic logic circuits

By applying mathematical models to describe the biochemical reactions of genetic systems, a synthetic genetic circuit with a specific function can be synthesized from the system's perspective.

Consider the dynamic model of the synthetic genetic logic circuit with L genes described by a class of nonlinear Hill differential equation [7]

$$\begin{aligned} \dot{m}_i &= \alpha_i f_i(u) - \lambda_i m_i + \alpha_{i,0}, \\ \dot{p}_i &= \beta_i m_i - \gamma_i p_i, \quad i = 1, \dots, L \end{aligned} \quad (1)$$

where m_i and p_i denote, respectively, concentrations of mRNA and protein for the gene i , λ_i and γ_i are, respectively, the degradation rates of mRNA and protein, α_i is the transcription rate of mRNA, β_i is the synthesis rate of protein, $\alpha_{i,0}$ is the basal production rate, $f_i(\cdot)$ is the promoter activity function which describes the nonlinear

transcriptional behavior and reflects the strength of the interaction between regulated protein and RNA polymerase (RNAP), and u is the concentration of transcription factor (TF) which is produced from other gene(s) or inducer(s) to control the transcription rate of target genes.

For a gene with an operator site which can bind a repressor or activator TF, the promoter activity functions are described as

$$f_{\text{NOT}}(u) = \frac{1}{1 + \left(\frac{u}{K}\right)^n} \quad (2)$$

and

$$f_{\text{Buffer}}(u) = \frac{\left(\frac{u}{K}\right)^n}{1 + \left(\frac{u}{K}\right)^n} \quad (3)$$

where f_{NOT} and f_{Buffer} are promoter activity functions for logic NOT and Buffer [26,30], respectively, n is the Hill coefficient which denotes the binding cooperativity between TF and the corresponding operator, and K is the Hill constant which are proportional to the lengths or affinities of the TF binding sites inserted into the promoter region of the target genes. For logic NOT gate, the input is a repressor and the gene produces a protein only in absence of the repressor; otherwise, the presence of the repressor obstructs the bound of RNAP and promoter. For genetic Buffer, the input is an activator which advances the bound of RNAP and promoter to produce protein. The frameworks for the two logic gates are illustrated in Figures 1(a) and (b), respectively.

For genes with two operator sites, which can bind two repressor TFs or activator TFs, the promoter activity

functions are described in accordance with their logic functions as

$$f_{\text{AND}}(u_1, u_2) = \frac{\left(\frac{u_1}{K_1}\right)^{n_1} \left(\frac{u_2}{K_2}\right)^{n_2}}{1 + \left(\frac{u_1}{K_1}\right)^{n_1} + \left(\frac{u_2}{K_2}\right)^{n_2} + \left(\frac{u_1}{K_1}\right)^{n_1} \left(\frac{u_2}{K_2}\right)^{n_2}} \quad (4)$$

$$f_{\text{OR}}(u_1, u_2) = \frac{\left(\frac{u_1}{K_1}\right)^{n_1} + \left(\frac{u_2}{K_2}\right)^{n_2} + \left(\frac{u_1}{K_1}\right)^{n_1} \left(\frac{u_2}{K_2}\right)^{n_2}}{1 + \left(\frac{u_1}{K_1}\right)^{n_1} + \left(\frac{u_2}{K_2}\right)^{n_2} + \left(\frac{u_1}{K_1}\right)^{n_1} \left(\frac{u_2}{K_2}\right)^{n_2}} \quad (5)$$

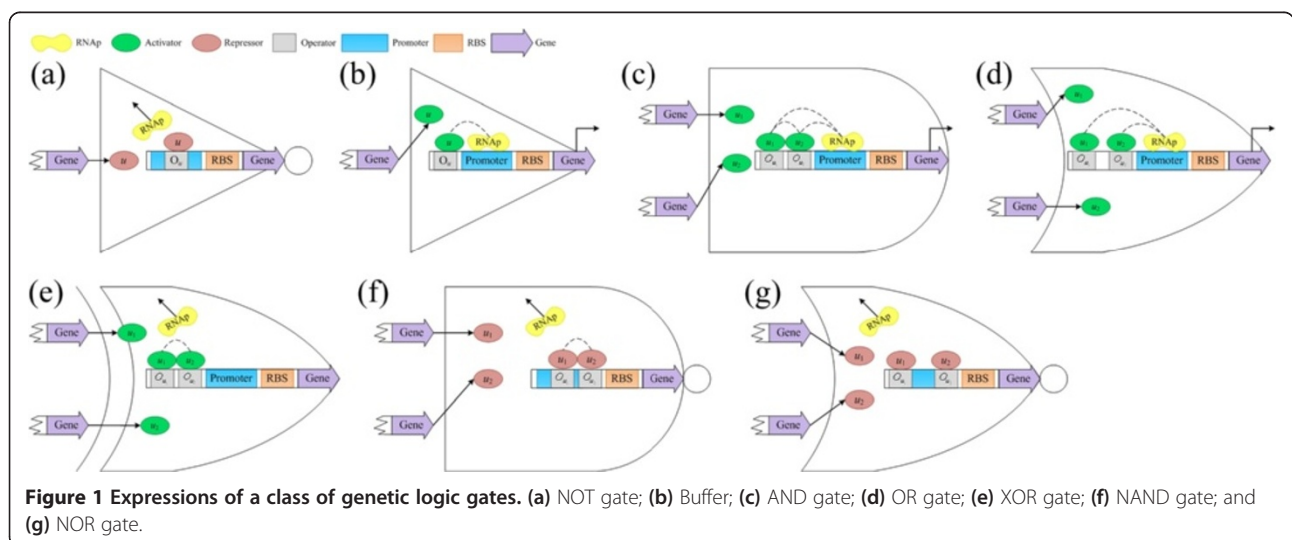
$$f_{\text{XOR}}(u_1, u_2) = \frac{\left(\frac{u_1}{K_1}\right)^{n_1} + \left(\frac{u_2}{K_2}\right)^{n_2}}{1 + \left(\frac{u_1}{K_1}\right)^{n_1} + \left(\frac{u_2}{K_2}\right)^{n_2} + \left(\frac{u_1}{K_1}\right)^{n_1} \left(\frac{u_2}{K_2}\right)^{n_2}} \quad (6)$$

$$f_{\text{NAND}}(u_1, u_2) = \frac{1 + \left(\frac{u_1}{K_1}\right)^{n_1} + \left(\frac{u_2}{K_2}\right)^{n_2}}{1 + \left(\frac{u_1}{K_1}\right)^{n_1} + \left(\frac{u_2}{K_2}\right)^{n_2} + \left(\frac{u_1}{K_1}\right)^{n_1} \left(\frac{u_2}{K_2}\right)^{n_2}} \quad (7)$$

and

$$f_{\text{NOR}}(u_1, u_2) = \frac{1}{1 + \left(\frac{u_1}{K_1}\right)^{n_1} + \left(\frac{u_2}{K_2}\right)^{n_2} + \left(\frac{u_1}{K_1}\right)^{n_1} \left(\frac{u_2}{K_2}\right)^{n_2}} \quad (8)$$

where f_{AND} , f_{OR} , f_{XOR} , f_{NAND} and f_{NOR} are, respectively, promoter activity functions of logic AND, OR, XOR, NAND and NOR gates, u_1 and u_2 are concentrations of repressor or activator TFs, K_1 and K_2 are Hill constants for u_1 and u_2 , respectively, and n_1 and n_2 are the corresponding



Hill coefficients. For logic AND, OR and XOR gates, the transcriptional behaviors are regulated by two activator TFs with different binding sites. Two repressor TFs control the genetic expressions of logic NAND and NOR gates. Their construction frameworks are shown in Figure 1(c)-(g).

In [38,39], the promoter and RBS are considered as a promoter-RBS part to regulate the genetic expression because the half-life of mRNA is shorter than the corresponding protein has. One can rewrite (1) as

$$\dot{p}_i = \rho_i f_i(u) - \gamma_i p_i + \rho_{0,i}, i = 1, \dots, L \quad (9)$$

where

$$\rho_i = \frac{\alpha_i \beta_i}{\lambda_i}, \rho_{0,i} = \frac{\alpha_{0,i} \beta_i}{\lambda_i}$$

Here, ρ_i and $\rho_{0,i}$ are new synthesis and basal production rates of the protein. The dynamic model of $2L$ differential equation (1) is reduced to the dynamic system with L differential equation (9). For real-world implementation, fetching the corresponding promoter-RBS parts from the promoter-RBS library, the synthetic genetic circuit can be realized in the genetic systems.

Synthetic genetic sequential logic circuits

In digital logic circuits, the output of sequential logic circuits depends not only on the present inputs but also on the past inputs. For synchronous sequential circuits, a clock signal is utilized as a metronome to coordinate actions of circuits, which oscillates between high-level and low-level states. The circuits with triggered clock signals become active either in the rising edge, the falling edge, or in both of the rising and falling edges. For the sequential logic circuit triggered at the rising edge of the clock signal, it becomes active when its clock pulse goes from low to high (0 to 1), and ignores high-to-low (1 to 0) transition.

In genetic logic circuits, oscillation signal produced from a repressilator is not ideal as a clock for use in the kind of circuits relying on the change of rising or falling edge of the clock signal for state transition. Our proposed approach is to introduce the idea of a waveform-shaping circuit in electronics to genetic logic circuits, and reshape the synthesized genetic oscillation signal into a crisp clock signal or a PWM signal with different duty cycles. By regulating the size of duty cycle, the clock pulse can be generated with a rising edge or a falling edge whose frequency is coherent to the oscillation frequency. To use the clock pulse, the designed genetic counter based on the topology of an electronic sequential logic circuit is triggered to generate a clock signal with its frequency is inversely integer multiple to the genetic oscillation.

Synthetic genetic oscillator

Oscillation phenomenon in biological systems has been discovered at various levels of biological organization. Its practical function is to control the dosage of drugs or as a synchronous mechanism for cell-cell communication. The oscillation capability depends not only on the network topology but also on the system parameters. Currently, the simplest synthetic genetic oscillator can be synthesized from a single gene repressing itself with a delayed negative feedback loop. An extension of the simplest oscillator, called a repressilator, consists of three genes (*lacI*, *tetR*, *cI*) which represses each other in the cycle chain. The product of the first repressor gene, *lacI* from *E. coli*, inhibits the transcription of the second repressor gene, *tetR* from the tetracycline-resistance transposon Tn10, whose protein product in turn inhibits the expression of the third repressor gene, *cI* from the λ phage. Finally, *cI* inhibits *lacI* expression, completing a negative feedback cycle [10]. The dynamic model of the repressilator can be described by

$$\dot{p}_i = \rho_i f_{\text{NOT},i}(p_j) - \gamma_i p_i \quad (10)$$

where p_i and p_j are concentrations of proteins for $(i, j) \equiv (lacI, cI), (tetR, lacI)$ or $(cI, tetR)$. For other design, the oscillation behavior can be generated by a number of repressor and activator genes in which the number of repressor genes must be odd.

To design the genetic oscillator with desired oscillations, one can realize a gene regulatory network to track a reference sinusoidal signal given by

$$y_d = A \sin(\omega_0 t + \varphi) + y_{d,0} \quad (11)$$

where y_d is the oscillation signal with the desired amplitude A , basal frequency ω_0 , phase φ and $y_{d,0}$ is the base level to ensure nonnegative protein concentration. For more details regarding synthetic genetic oscillator design by optimization algorithms one is referred to [35].

Waveform-shaping circuit

In electronics, a waveform-shaping circuit is designed to shape the input signal to the desired form according to an input and output (I/O) characteristic curve. For the oscillation input and the clock output, the I/O characteristic curve of the desired waveform-shaping circuit is displayed in Figure 2. A step function (dashed line) with a threshold level y_T is used in electronics. For the input signal with its value larger than the threshold level, it is treated as "logic high". Otherwise, it is referred to "logic low". However, in biological systems, an ideal step function doesn't exist. A sigmoid function (solid line in Figure 2) might be used instead. From the I/O characteristic curve of a sigmoid function, there are two operational regions:

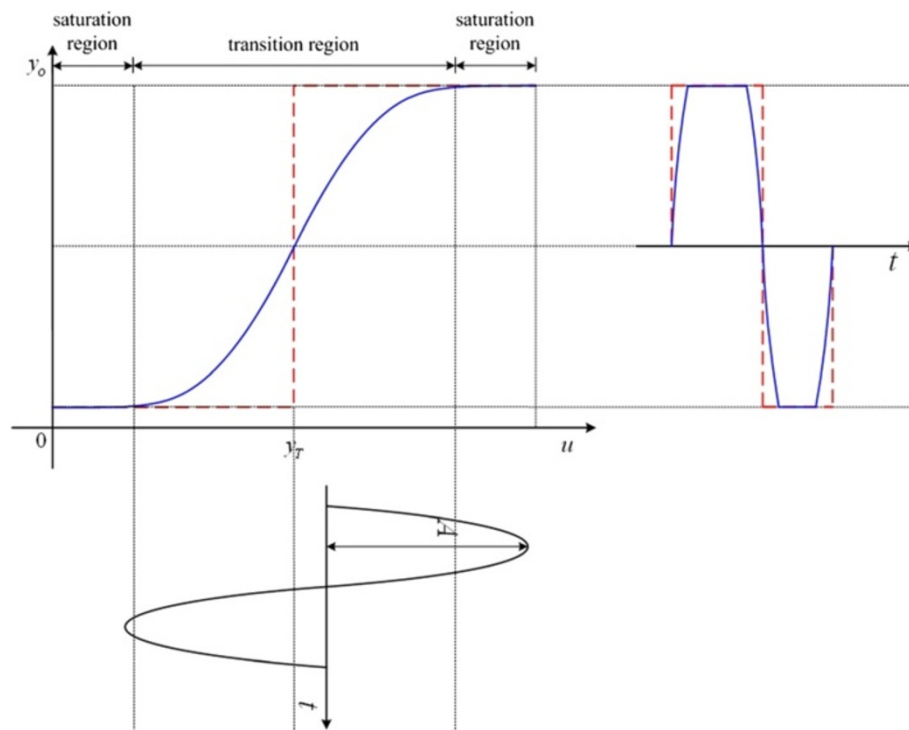


Figure 2 Idea of a genetic waveform shaping circuit.

saturation and transition. The input signal in the saturation region can be cut-off and hold on the high level or the low level for approximation. In the transition region, the gain in the operation point y_T must be more than (normalized) 1 because it ensures that the input which is larger or less than the threshold level will be amplified or shrunk. By cascading the next sigmoid function, the oscillation input signal will gradually reach the saturation region and remain in the high or low level.

According to this idea, a waveform-shaping circuit can be used to regulate the period of the logic high/low levels of an oscillation signal in a sinusoidal cycle and generate a PWM signal with different duty cycle defined by

$$D = \frac{T_{on}}{T_0} \cdot 100\% \quad (12)$$

where D is the duty cycle, T_0 is the basal period of oscillation signal (11) with $2\pi/\omega_0$ and T_{on} being the period of “logic high” in a basal period. For the PWM signals with different duty cycles, the threshold is obtained by considering

$$y_T = A \sin(\omega_0 t + \varphi) + y_{d,0}, t = t_h \pm \frac{T_{on}}{2} \quad (13)$$

with

$$t_h = \frac{1}{\omega_0} \sin^{-1}(1) - \frac{\varphi}{\omega_0}, t_h \in [0, T_0] \quad (14)$$

To select the threshold level approaching to $y_{d,0} + A$, a clock pulse served as a rising triggered edge is generated and shown in Figure 3(a). For the clock pulse regarding as a falling triggered edge shown in Figure 3(b), one can choose the threshold level which is close to $y_{d,0} - A$. Similarly, a PWM signal with 50% duty cycle, i.e. the clock signal with its frequency is consistent to the genetic oscillator, is synthesized and shown in Figure 3(c) while selecting the base level of the signal $y_{d,0}$. In other words, the PWM signals with different duty cycles can be synthesized from an oscillation signal via a waveform-shaping circuit in different threshold levels.

Realizing a genetic waveform-shaping circuit

In engineered genetic logic circuits, a genetic Buffer [30] is proposed to serve as a buffer between two cascade genetic circuits to enhance logic signal transfer. It's used here to aid the genetic waveform-shaping circuit design:

$$\dot{p}_k = \rho_k f_{\text{Buffer},k}(u_k, K_k, n_k) - \gamma_k p_k + \rho_{0,k}, k = 1, \dots, M \quad (15)$$

Its steady-state solution is easily obtained as

$$p_{k,ss} = \frac{\rho_k}{\gamma_k} f_{\text{Buffer},k}(u_k, K_k, n_k) + \frac{\rho_{0,k}}{\gamma_k}, k = 1, \dots, M \quad (16)$$

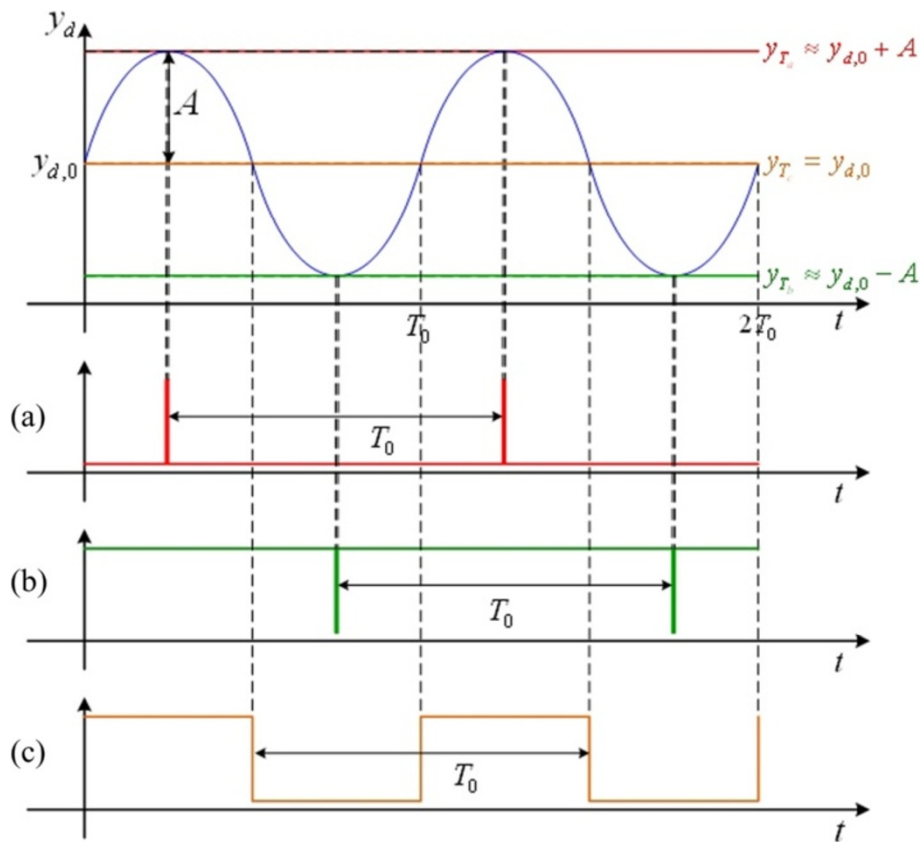


Figure 3 Ideal PWM signals. (a) a rising triggered edge; (b) a falling triggered edge; and (c) 50% duty cycle.

where p_k is the output concentration of the k th Buffer, $p_{k,ss}$ denotes its steady-state concentration, u_k , K_k and n_k are, respectively, the input concentration, Hill constant, and Hill coefficient of the k th Buffer and ρ_k , γ_k and $\rho_{0,k}$ are, respectively, synthesis, decay and basal rates. The second term of the right-hand side of (16) is the minimal level and ρ_k/γ_k is the difference between the minimal and maximal levels. Output concentration of the genetic Buffer is the half maximal output concentration when the input concentration equals K_k and thus K_k refers to the threshold level y_T .

In each stage, the corresponding inputs and the threshold levels are given by

$$u_k = \begin{cases} y_d, & k = 1 \\ p_{k-1}, & 1 < k \leq M \end{cases} \quad (17)$$

and

$$K_k = \begin{cases} y_T, & k = 1 \\ \frac{\rho_{k-1} + \rho_{0,k-1}}{2\gamma_{k-1}}, & 1 < k \leq M \end{cases} \quad (18)$$

In the first stage, the input signal is the oscillation signal in (11) and the threshold level is chosen according to the desired duty cycle in (13). For the next stage, the

input signal is the output concentration of the previous Buffer and the threshold level is the half maximal output level in the previous one. The topology of our proposed genetic waveform-shaping circuit is displayed in Figure 4. The oscillation signal from protein production of any gene of the repressilator activates the first gene in the genetic waveform-shaping circuit, whose production activates the next gene. Stage by stage, the oscillation can be reshaped to the crisp clock signal or PWM signal. However, the problem of slow convergence to the maximal level is occurred for the larger threshold level K_k . To resolve this problem, one can again cascade a Buffer with the design parameters of (16) in the last stage of genetic waveform-shaping circuit to compensate the output level.

The gain at the operating point K_k is obtained by

$$A_k = \left. \frac{\partial p_{k,ss}}{\partial u_k} \right|_{u_k=K_k} = \frac{\rho_k n_k}{4\gamma_k K_k} \quad (19)$$

where A_k is the gain of the k th Buffer. The gain is proportional to the Hill coefficient n_k and the synthesis rate ρ_k and is inversely proportional to the Hill constant K_k and the decay rate γ_k at the operating point $u_k = K_k$. To ensure that the necessary condition of the gain at the

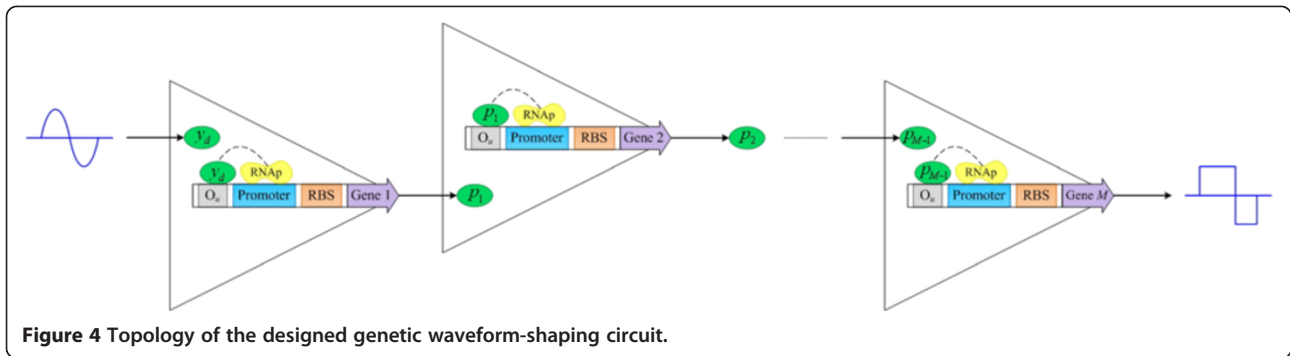


Figure 4 Topology of the designed genetic waveform-shaping circuit.

operating point, K_k should be exceeding 1. At first, one chooses the appropriate Hill constant for the desired synthesized PWM signal and then selects a suitable Hill coefficient n_k , synthesis rate ρ_k and decay rate γ_k satisfying (19). From the system parameters in the previous stage, one proceeds to choose the appropriate system parameters in the next stage satisfying (18) and (19). From [38,39], to realize the proposed genetic logic circuit in reality, one can find applicable promoter-RBS components from the constructed promoter-RBS library, whose I/O characteristic curves are capable of satisfying (18) and (19).

Design of genetic frequency divider circuit

Frequency divider in electronics is a device that generates an output signal whose frequency is an inverse multiple to that of the input signal. A sequential logic circuit, counter, is used to achieve this function, which is constructed by a series of flip-flops and triggered by the clock pulse to generate the clock signals with multi-fold basal period. Figure 5 illustrates an ideal clock signal

while triggering at the rising edge of the clock signal with the desired basal period.

Genetic JK flip-flop

Genetic JK flip-flops based on the topology of digital logic circuits in electronics divide into the rising edge-triggered one and the falling edge-triggered one shown as in Figure 6. For the rising edge-triggered genetic JK flip-flop, its model is described by

$$\begin{aligned}
 \dot{p}_W &= \rho_W f_{\text{AND}}(p_K, p_{\text{CLK}}, K_W, n_W) - \gamma_W p_W, \\
 \dot{p}_V &= \rho_V f_{\text{AND}}(p_J, p_{\text{CLK}}, K_V, n_V) - \gamma_V p_V, \\
 \dot{p}_R &= \rho_R f_{\text{AND}}(p_W, p_Q, K_R, n_R) - \gamma_R p_R, \\
 \dot{p}_S &= \rho_S f_{\text{AND}}(p_V, p_{\bar{Q}}, K_S, n_S) - \gamma_S p_S, \\
 \dot{p}_Q &= \rho_Q f_{\text{NOR}}(p_R, p_{\bar{Q}}, K_Q, n_Q) - \gamma_Q p_Q, \\
 \dot{p}_{\bar{Q}} &= \rho_{\bar{Q}} f_{\text{NOR}}(p_S, p_Q, K_{\bar{Q}}, n_{\bar{Q}}) - \gamma_{\bar{Q}} p_{\bar{Q}}
 \end{aligned} \tag{20}$$

where p_{CLK} is the concentration of clock pulse from low to high, p_W , p_V , p_R , p_S , p_Q , and $p_{\bar{Q}}$ denote, respectively, the protein concentrations of the genes W , V , R , S , Q and \bar{Q} . The rising edge-triggered genetic JK flip-flop

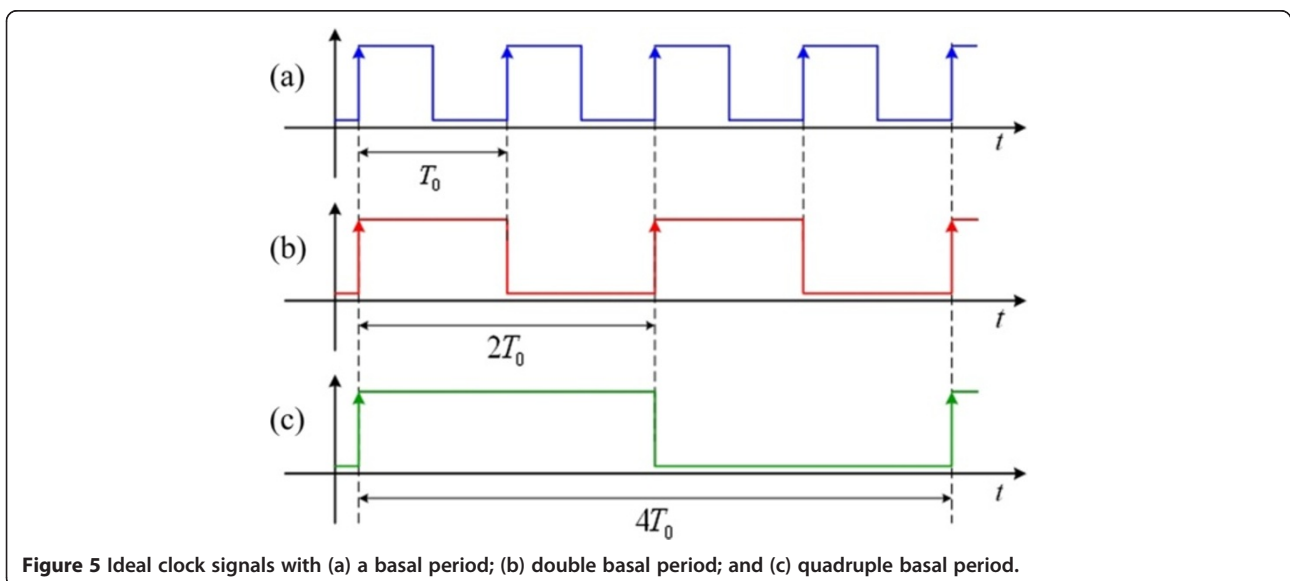
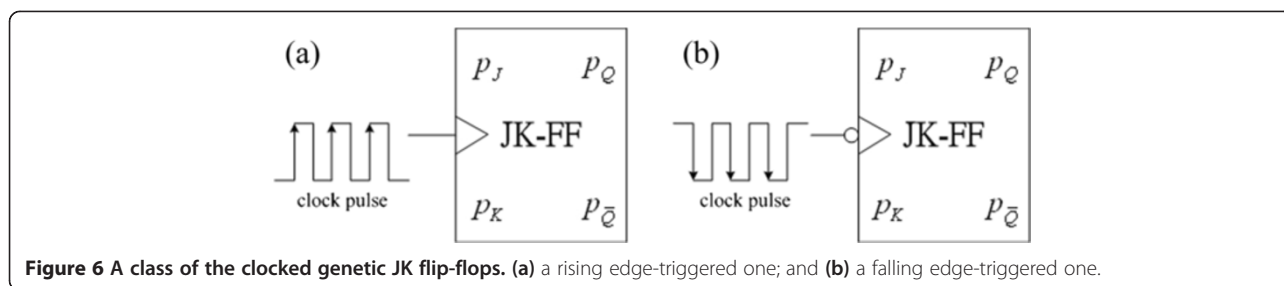


Figure 5 Ideal clock signals with (a) a basal period; (b) double basal period; and (c) quadruple basal period.



becomes active only when the clock pulse goes from low to high. There are four genetic AND gates and two NOR gates and the topology is displayed in Figure 7. The proteins p_K and p_{CLK} activate the transcription of the gene W . The proteins p_J and p_{CLK} activate the transcription of the gene V . The productions of the genes W and Q activate the transcription of the gene R and the productions of the genes V and Q activate the transcription of the gene S . The proteins p_R and p_{Q} inhibit the transcription of the gene Q and the proteins p_S and p_Q inhibit the transcription of the gene Q .

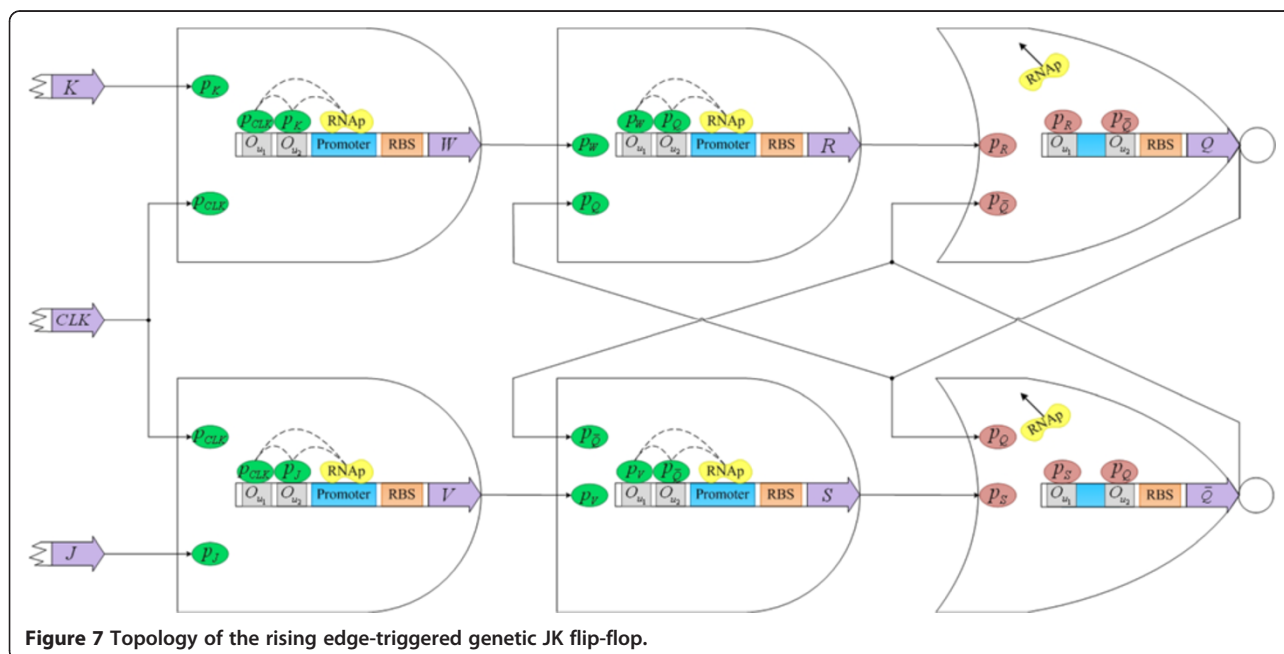
For the falling edge-triggered genetic JK flip-flop, the model is described by

$$\begin{aligned}
 \dot{p}_W &= \rho_W f_{\text{NAND}}(p_K, p_{CLK}, K_W, n_W) - \gamma_W p_W, \\
 \dot{p}_V &= \rho_V f_{\text{NAND}}(p_J, p_{CLK}, K_V, n_V) - \gamma_V p_V, \\
 \dot{p}_R &= \rho_R f_{\text{NAND}}(p_W, p_Q, K_R, n_R) - \gamma_R p_R, \\
 \dot{p}_S &= \rho_S f_{\text{NAND}}(p_V, p_Q, K_S, n_S) - \gamma_S p_S, \\
 \dot{p}_Q &= \rho_Q f_{\text{NAND}}(p_S, p_Q, K_Q, n_Q) - \gamma_Q p_Q, \\
 \dot{p}_{\bar{Q}} &= \rho_{\bar{Q}} f_{\text{NAND}}(p_R, p_Q, K_{\bar{Q}}, n_{\bar{Q}}) - \gamma_{\bar{Q}} p_{\bar{Q}}
 \end{aligned}
 \tag{21}$$

where p_{CLK} is the concentration of clock pulse from high to low. This circuit is composed of six genetic NAND gates with its topological structure shown in Figure 8.

Genetic counter

To synthesize the clock signal with an inversely multiple of frequency of genetic oscillator, a synchronous genetic counter circuit can be used. The counter circuit in electronics works on the rising or falling edge of the clock and count the number of clock pulses. Based on the feature, one first generates a series of clock pulses using our proposed genetic waveform-shaping circuit, and uses the clock pulse signal to trigger the genetic counter. According to the Karnaugh map in the digital logic theory, the input signals of each genetic JK flip-flop and the topology of genetic counter circuit can be determined. To synthesize the clock signals with 2^δ -fold basal period in which δ is a positive integer, a synchronous genetic counter circuit with δ number of rising edge-triggered genetic JK flip-flops is constructed by



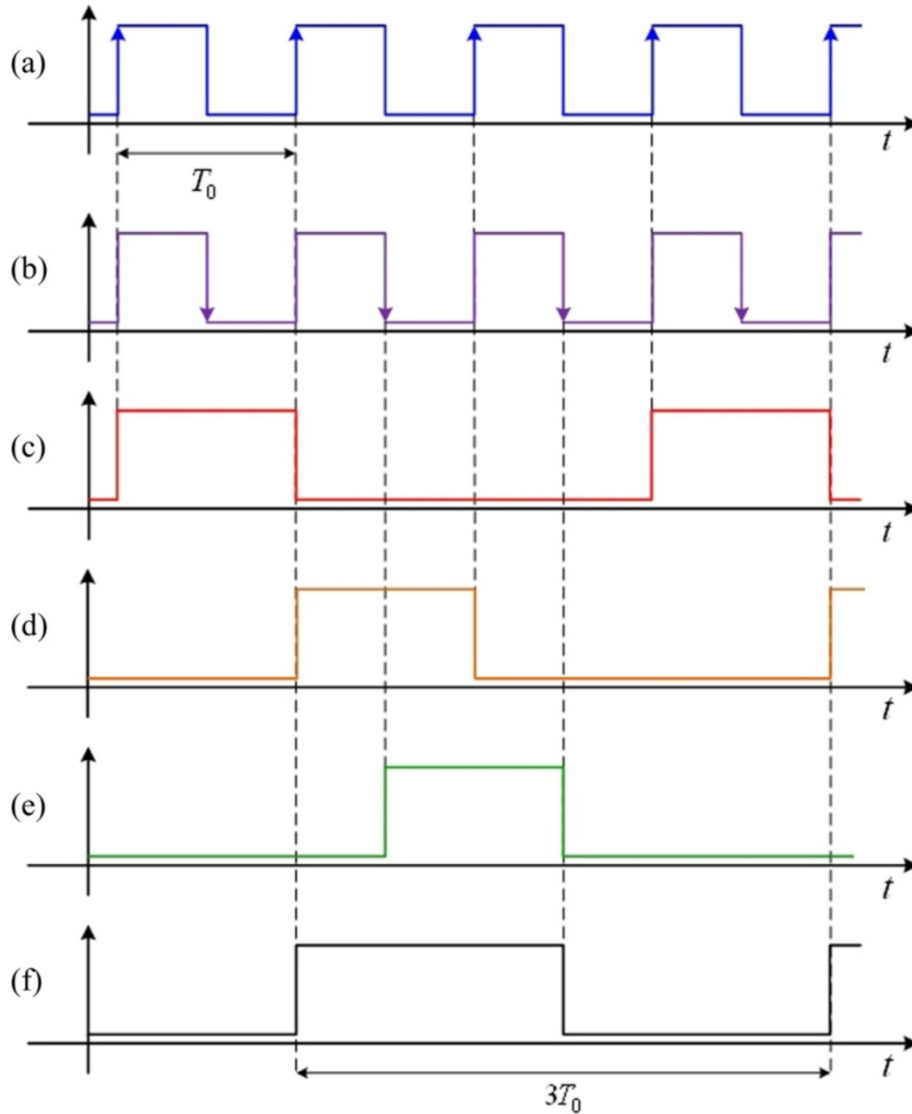


Figure 11 Ideal signals for synthesizing the clock signal with triple basal period. (a) Clock pulse signal from low to high; (b) Clock pulse signal from high to low; (c) Output signal p_{Q_1} of the first genetic JK flip-flop; (d) Output signal p_{Q_2} of the second genetic JK flip-flop; (e) Output signal p_{Q_3} of the third genetic JK flip-flop; and (f) Output signal of logic OR of (d) and (e).

Similarly, the clock signal with $D = 90\%$ is generated by the following genetic waveform-shaping circuit

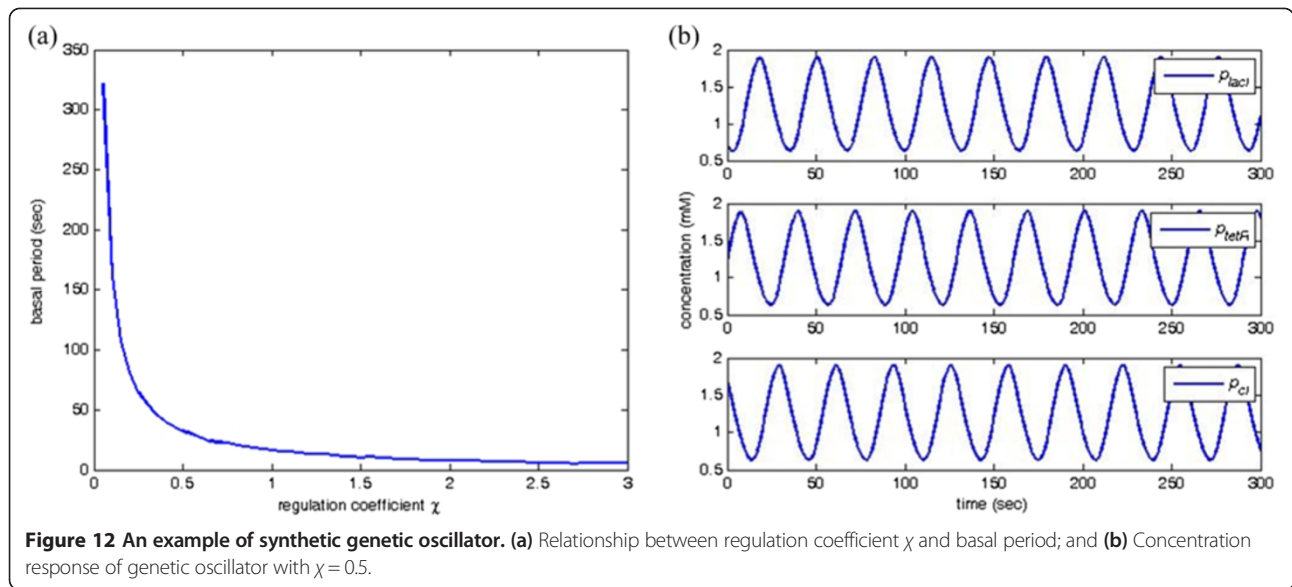
$$\begin{aligned}
 \dot{p}_1 &= f_{\text{Buffer}}(p_{cl}, 0.6761, 4) - p_1, \\
 \dot{p}_2 &= f_{\text{Buffer}}(p_1, 0.5, 4) - p_2, \\
 \dot{p}_3 &= f_{\text{Buffer}}(p_2, 0.5, 4) - p_3, \\
 \dot{p}_4 &= f_{\text{Buffer}}(p_3, 0.5, 4) - p_4, \\
 \dot{p}_5 &= f_{\text{Buffer}}(p_4, 0.5, 4) - p_5, \\
 \dot{p}_6 &= 1.1083f_{\text{Buffer}}(p_5, 0.5, 4) - p_6
 \end{aligned} \tag{28}$$

where p_6 is the clock pulse signal with $D = 90\%$. The first Buffer is designed with the threshold level 0.6761 and the second to the fifth Buffers choose the corresponding threshold level 0.5 satisfying (18). The Buffer in the last

stage functions to compensate the output with the maximal level. In each stage, the I/O characteristic curves are shown in Figure 14(a) and the output concentrations are shown in Figure 14(b).

According the proposed approach, the oscillation signal can be rectified and filtered as a direct current (DC) output. The genetic waveform-shaping circuit with $D = 100\%$ is described by

$$\begin{aligned}
 \dot{p}_1 &= f_{\text{Buffer}}(p_{cl}, 0.5, 4) - p_1, \\
 \dot{p}_2 &= f_{\text{Buffer}}(p_1, 0.5, 4) - p_2, \\
 \dot{p}_3 &= f_{\text{Buffer}}(p_2, 0.5, 4) - p_3, \\
 \dot{p}_4 &= 1.0851f_{\text{Buffer}}(p_3, 0.5, 4) - p_4,
 \end{aligned} \tag{29}$$



where p_4 is the DC signal with $D = 100\%$. There are four genetic Buffers. In the first Buffer, the threshold level 0.5 is chosen and the second to fifth Buffers choose the threshold level 0.5. The Buffer in the last stage functions to compensate the output of maximal level. In each stage, the I/O characteristic curves are shown in Figure 15(a) and the output concentrations are shown in Figure 15(b).

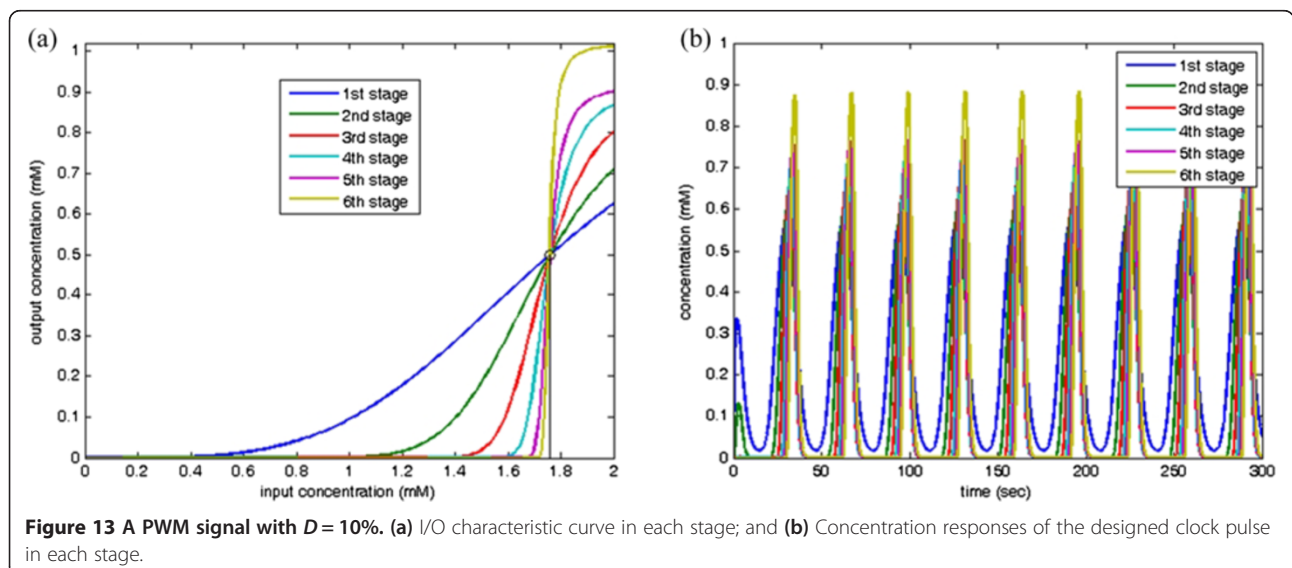
Synthesis of clock signals

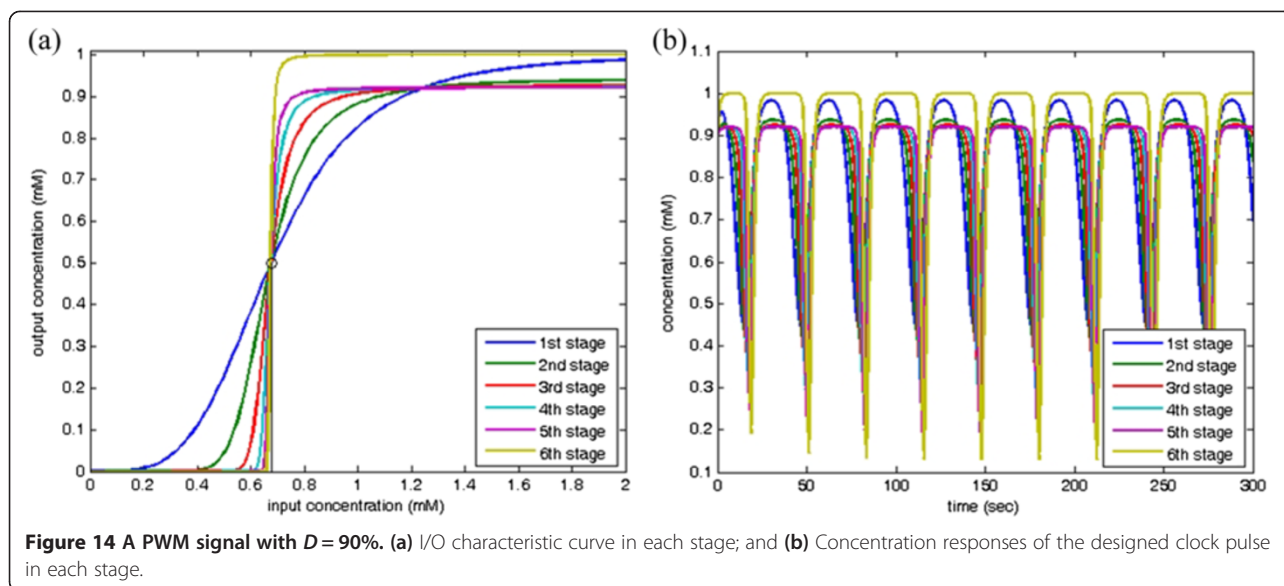
To design the clock signals with the double and the quadruple basal periods, we suppose $\delta = 2$, all synthesis and decay rates are 1, all Hill constants are 0.5, all Hill coefficients are 4 in (22), p_{CLK_1} is the clock pulse generated from (27) and the high level DC signal is synthesized by (29).

The concentration responses of the designed clock signals with double and quadruple basal periods are shown in Figure 16(a). State change of the genetic JK flip-flop occurs when the clock pulse goes from 0 to 1. For the clock signal with triple basal period, suppose that all variables remain the same to the above example, p_{CLK_1} and p_{CLK_2} are, respectively, the clock pulses generated in (27) and (28), and the DC signal is synthesized by (29). Concentration response of the designed clock signal with the triple basal period is successfully generated, shown as in Figure 16(b).

Discussion

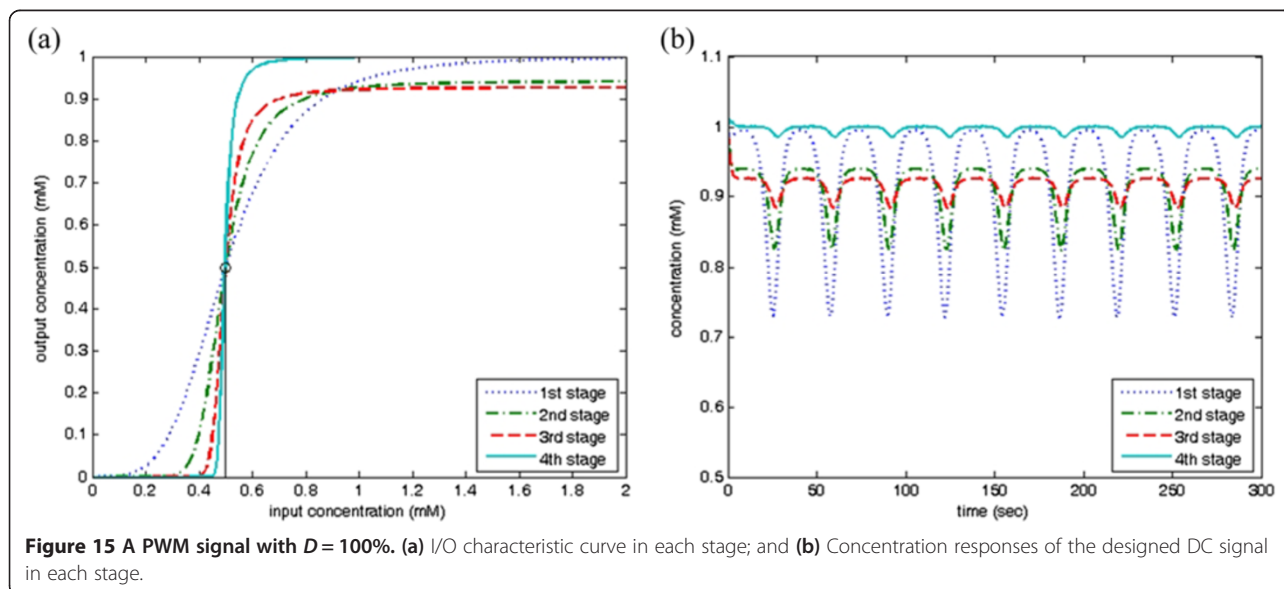
The goal of synthetic biology is to design genetic circuits with specific functions by using the approaches of





mathematics and engineering. Several genetic logic gates have recently been developed and experimentally realized [16-22]. In [16-18], simplified schemes of various genetic logic gates have been constructed by using basic biological components such as activator/repressor genes, reporter gene, promoter and RBS. For a gene with multiple binding sites, the transcription rate of the gene can be regulated by the same number of regulated proteins. The modified protease can control the degradation rate of protein [19]. For logic NOT gate, the gene with a repressor regulated protein can generate the I/O characteristic curves with an inverse sigmoid function. A complementary form of NOT gate is "Buffer" [30] which is regulated by an activator protein to reshape the I/O

characteristic curves for enhancing sharpness of a biologic response with a sigmoidal function. For logic AND gate with two binding sites, the genetic expression can be regulated by two activator proteins. In [22], the activator proteins HrpR and HrpS control the promoter of the corresponding reporter gene to realize logic AND gate. By cascading a NOT gate in the outputs of AND/OR gates, the NAND/NOR gates can be synthesized [20-22]. In [22], a reporter gene with different promoters (lac, BAD and lux) and different RBS parts (rbs30, rbs33, rbsH, etc) can determine the values of Hill constant and coefficient and achieve the different NOT gates with different threshold levels. Similarly, one also can choose different promoter and RBS components with repressor



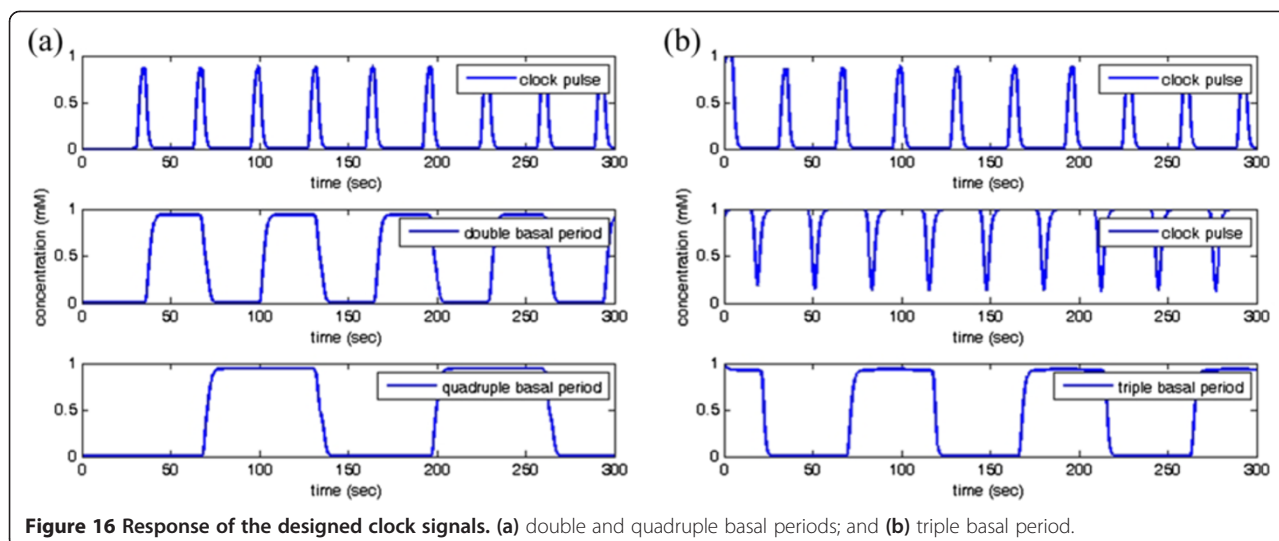


Figure 16 Response of the designed clock signals. (a) double and quadruple basal periods; and (b) triple basal period.

or activator proteins to construct the corresponding system parameters for other logic gates. Through synchronous cascades of these genetic logic gates based on the topology of electronic circuits, the toggle switch and oscillator have been characterized in real world by a class of Hill differential equations, and realized in *Escherichia coli* [9,10]. The previous papers have exhibited the possibility of realization of a class of fundamental biological devices in real world which form a basis for implementing more complicated combinational or sequential biological circuits. To realize the proposed genetic waveform-shaping circuit and genetic counter using a systematic approach, we first can identify the different promoter-RBS parts to achieve the basic logic gates, and then assemble these logic gates with the topologies in Figures 4, 7, 8, 9, and 10.

Reporter protein or green fluorescent protein (GFP) has been used in real-world experiments to reflect genetic expression [41]. By using a flow cytometer, the intensity of fluorescence of GFP is measurable. The system parameters in the dynamic model of genetic circuits can be identified from these measurement data using system identification methods [22]. In [38,39], a class of robust genetic circuits has been constructed by selecting the applicable promoter-RBS component from a promoter-RBS library. Therefore, to realize our proposed genetic logic circuits including the genetic waveform-shaping circuit and genetic counter, one first establishes the measurement device which exhibits fluorescence concentrations of a series of a repressor or activator gene with different promoter-RBS components and TFs via fluorescence measurement [41] and then rebuild a promoter-RBS library with information of system parameters in terms of our mathematical model describing the behaviors of genetic logic gates by using the system identification methods [30,35]. To select the adequate promoter-RBS component generating the I/O

characteristic curves of Buffers [30] and satisfying the designed conditions (18) and (19) from the rebuilt promoter-RBS library via an optimization algorithm (such as [35]), the clock pulse signals can be synthesized based on the proposed cascaded genetic logic circuit. Similarly, one can also choose the suitable promoter-RBS components to realize the various genetic logic gates and assemble these logic gates based the proposed topology. The designed genetic counters can be triggered by the synthesized clock pulse signals to generate the clock signals with multi-basal periods.

In sequential logic circuits, the triggered signals could be rising or falling edge of a clock signal. One can use clock pulse signals to replace the rising or falling edge to trigger the sequential logic circuits such as our proposed genetic counters. The proposed genetic counters with a function of frequency divider based on the topology of electronic circuits can be used to generate clock signals with multi-basal period. In other words, one can utilize the proposed approach to generate clock signals whose periods is an integer multiple of 24-hour from a cell with circadian rhythm.

Conclusions

This paper has proposed a synthetic genetic sequential logic circuit as a frequency divider. The synthesized clock frequency is inversely multiple to that of the genetic oscillator which generates the fundamental sine wave. Through controlling threshold level of the genetic Buffer, the proposed waveform-shaping circuit regulates time duration of logic high/low levels in a basic sinusoidal cycle for an oscillation input and generates ideal pulse signals with the coherent frequency to the genetic oscillator. Regarding the generated pulse signal as rising/falling edge of the clock signal with base frequency, a genetic synchronous counter circuit based on the topology of digital sequential logic

circuit is triggered by a pulse wave form to synthesize a clock signal with the inverse multiple frequency to the genetic oscillator. Experimental results *in silico* show the synthesizing genetic clock with double, triple, and quadruple basal periods while operating based on a genetic oscillator. By extending the proposed design principle, a class of multi-basal period clock signals can be generated in a straightforward manner.

Competing interests

The authors declare that they have no competing interests.

Authors' contributions

CLL formulated the research topic. CHC developed the method, performed the simulation and wrote the manuscript. Both authors read and approved the manuscript.

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